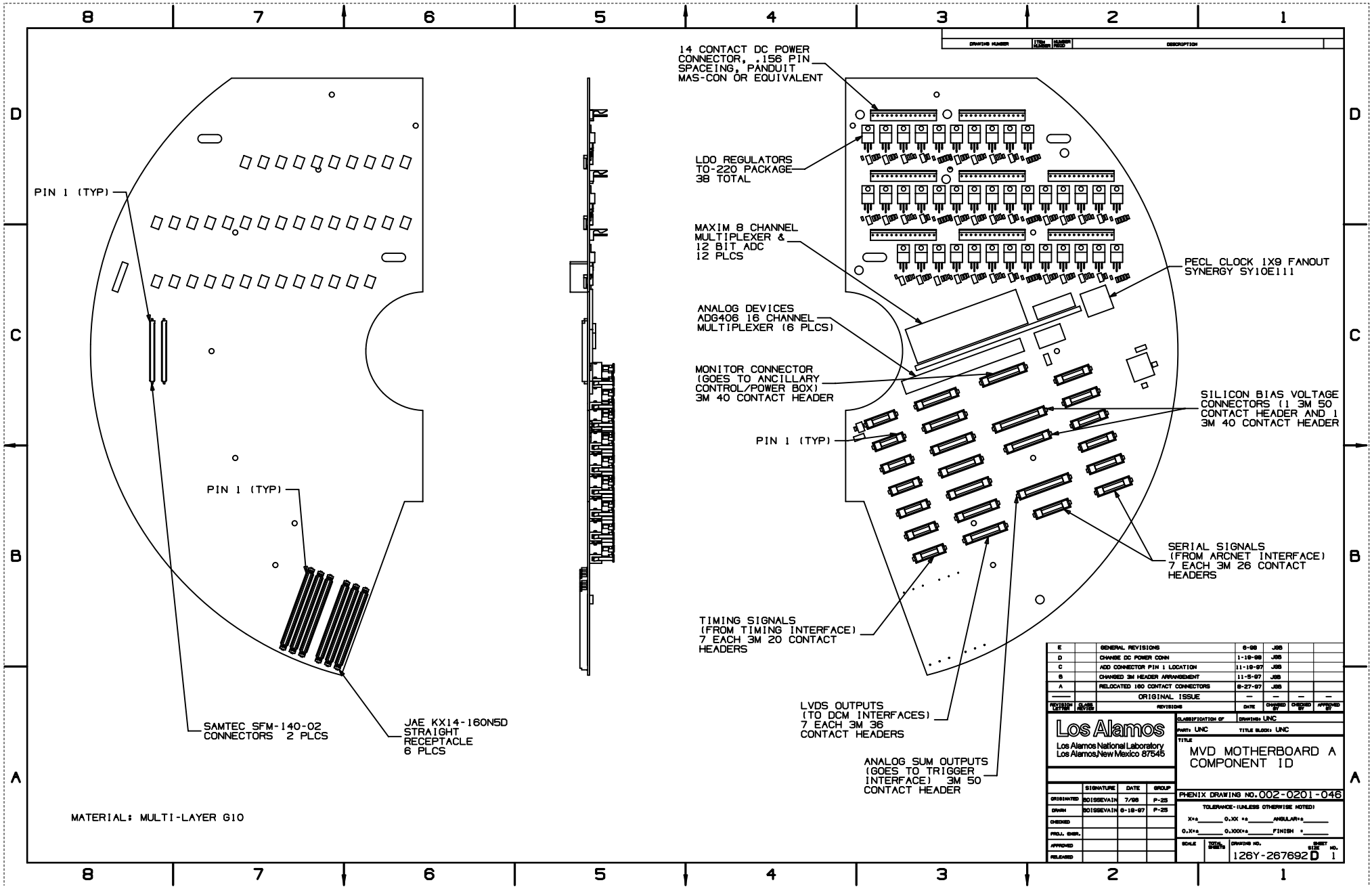
14 CONTACT DC POWER CONNECTOR, .156 PIN SPACEING, PANDUIT MAS-CON OR EQUIVALENT
LDO REGULATORS TO-220 PACKAGE 38 TOTAL
MAXIM 8 CHANNEL MULTIPLEXER & 12 BIT ADC 12 PLCS
PECL CLOCK 1X9 FANOUT SYNERGY SY10E111
ANALOG DEVICES ADG406 16 CHANNEL MULTIPLEXER (6 PLCS)
MONITOR CONNECTOR (GOES TO ANCILLARY CONTROL/POWER BOX) 3M 40 CONTACT HEADER
SILICON BIAS VOLTAGE CONNECTORS (1 3M 50 CONTACT HEADER AND 1 3M 40 CONTACT HEADER
PIN 1 (TYP)
SERIAL SIGNALS (FROM ARCNET INTERFACE) 7 EACH 3M 26 CONTACT HEADERS
TIMING SIGNALS (FROM TIMING INTERFACE) 7 EACH 3M 20 CONTACT HEADERS
LVDS OUTPUTS (TO DCM INTERFACES) 7 EACH 3M 36 CONTACT HEADERS
ANALOG SUM OUTPUTS (GOES TO TRIGGER INTERFACE) 3M 50 CONTACT HEADER
PIN 1 (TYP)
PIN 1 (TYP)
SAMTEC SFM-140-02 CONNECTORS 2 PLCS
JAE KX14-160N5D STRAIGHT RECEPTACLE 6 PLCS
MATERIAL: MULTI-LAYER G10
DRAWING NUMBER
ITEM NUMBER
NUMBER REQD
DESCRIPTION
E GENERAL REVISIONS 6-98 JSB
D CHANGE DC POWER CONN 1-16-98 JSB
C ADD CONNECTOR PIN 1 LOCATION 11-19-97 JSB
B CHANGED 3M HEADER ARRANGEMENT 11-5-97 JSB
A RELOCATED 160 CONTACT CONNECTORS 8-27-97 JSB
ORIGINAL ISSUE
REVISION LETTER
ECAD REVISION
REVISIONS
DATE
CHANGED BY
CHECKED BY
APPROVED BY
Los Alamos
Los Alamos National Laboratory
Los Alamos,New Mexico 87545
CLASSIFICATION OF:
DRAWING: UNC
PART: UNC
TITLE BLOCK: UNC
TITLE
MVD MOTHERBOARD A COMPONENT ID
SIGNATURE DATE GROUP
ORIGINATED BOISSEVAIN 7/98 P-25
DRAWN BOISSEVAIN 6-18-97 P-25
CHECKED
PROJ. ENGR.
APPROVED
RELEASED
PHENIX DRAWING NO. 002-0201-046
TOLERANCE (UNLESS OTHERWISE NOTED)
X ± ____ 0.XX ± ____ ANGULAR ± ____
0.X ± ____ 0.XXX ± ____ FINISH ± ____
SCALE
TOTAL SHEETS
DRAWING NO.
SHEET SIZE
NO.
126Y-267692 D 1