

	1	2	3	4	5	6	7	8
A	MOTHERBOARD CONNECTOR GROUP4		MOTHERBOARD CONNECTOR GROUP5		MOTHERBOARD CONNECTOR GROUP6			
	J38-1 1 G4_MB0 J38-2 2 G4_MB1 J38-3 3 G4_MB2 J38-4 4 G4_MB3 J38-5 5 G4_MB4 J38-6 6 G4_MB5 J38-7 7 G4_DGND J38-8 8 G4_DGND J38-9 9 G4_L1A J38-10 10 G4_9.5MHZ_CLK J38-11 11 G4_DGND J38-12 12 G4_DGND J38-13 13 G4_PECCL-DIFF_CLKA J38-14 14 G4_PECCL-DIFF_CLKB J38-15 15 G4_MCM1_LVDS_DATA_OUT_CLKA J38-16 16 G4_MCM1_LVDS_DATA_OUT_CLKB J38-17 17 G4_MCM1_LVDS_DATA_OUT_CLKA J38-18 18 G4_MCM1_LVDS_DATA_OUT_CLKB J38-19 19 G4_MCM1_LVDS_DATA_OUT_2A J38-20 20 G4_MCM1_LVDS_DATA_OUT_2B J38-21 21 G4_MCM1_LVDS_DATA_OUT_1A J38-22 22 G4_MCM1_LVDS_DATA_OUT_1B J38-23 23 G4_MCM2_LVDS_DATA_OUT_1A J38-24 24 G4_MCM2_LVDS_DATA_OUT_1B J38-25 25 G4_MCM2_LVDS_DATA_OUT_2A J38-26 26 G4_MCM2_LVDS_DATA_OUT_2B J38-27 27 G4_MCM3_LVDS_DATA_OUT_1A J38-28 28 G4_MCM3_LVDS_DATA_OUT_1B J38-29 29 G4_MCM3_LVDS_DATA_OUT_2A J38-30 30 G4_MCM3_LVDS_DATA_OUT_2B J38-31 31 G4_MCM4_LVDS_DATA_OUT_1A J38-32 32 G4_MCM4_LVDS_DATA_OUT_1B J38-33 33 G4_MCM4_LVDS_DATA_OUT_2A J38-34 34 G4_MCM4_LVDS_DATA_OUT_2B J38-35 35 G4_MCM5_LVDS_DATA_OUT_1A J38-36 36 G4_MCM5_LVDS_DATA_OUT_1B J38-37 37 G4_MCM5_LVDS_DATA_OUT_2A J38-38 38 G4_MCM5_LVDS_DATA_OUT_2B J38-39 39 G4_MCM6_LVDS_DATA_OUT_1A J38-40 40 G4_MCM6_LVDS_DATA_OUT_1B J38-41 41 G4_MCM6_LVDS_DATA_OUT_2A J38-42 42 G4_MCM6_LVDS_DATA_OUT_2B J38-43 43 G4_DGND J38-44 44 G4_MCM1_S_EN J38-45 45 G4_MCM2_S_EN J38-46 46 G4_MCM3_S_EN J38-47 47 G4_MCM4_S_EN J38-48 48 G4_MCM5_S_EN J38-49 49 G4_DGND J38-50 50 G4_SERIAL_CLK J38-51 51 G4_DGND J38-52 52 G4_S_DATA_OUT_MCM1 J38-53 53 G4_S_DATA_OUT_MCM2 J38-54 54 G4_S_DATA_OUT_MCM3 J38-55 55 G4_S_DATA_OUT_MCM4 J38-56 56 G4_S_DATA_OUT_MCM5 J38-57 57 G4_DGND J38-58 58 G4_S_LATCH J38-59 59 G4_READBACK_EN J38-60 60 G4_XILINX_PROGRAM J38-61 61 PC4_TEMP_MON J38-62 62 G4_MCM1_PREAMP_OUT_SPY J38-63 63 G4_MCM2_PREAMP_OUT_SPY J38-64 64 G4_MCM3_PREAMP_OUT_SPY J38-65 65 G4_MCM4_PREAMP_OUT_SPY J38-66 66 G4_MCM5_PREAMP_OUT_SPY J38-67 67 G4_MCM6_PREAMP_OUT_SPY J38-68 68 MB_AGND J38-69 69 G4_MCM1_DS J38-70 70 G4_MCM2_DS J38-71 71 G4_MCM3_DS J38-72 72 G4_MCM4_DS J38-73 73 G4_MCM5_DS J38-74 74 G4_DGND J38-75 75 G4_5VD J38-76 76 G4_DGND J38-77 77 G4_5VD J38-78 78 G4_DGND J38-79 79 G4_5VD J38-80 80 G4_DGND J38-81 81 G4_5VD J38-82 82 G4_DGND J38-83 83 G4_5VD J38-84 84 G4_DGND J38-85 85 G4_5VD J38-86 86 G4_DGND J38-87 87 G4_5VD J38-88 88 G4_DGND J38-89 89 G4_5VD J38-90 90 G4_DGND J38-91 91 G4_5VD J38-92 92 G4_DGND J38-93 93 G4_5VD J38-94 94 G4_DGND J38-95 95 G4_5VD J38-96 96 G4_DGND J38-97 97 G4_5VD J38-98 98 G4_DGND J38-99 99 G4_5VD J38-100 100 G4_DGND J38-101 101 G4_5VD J38-102 102 G4_DGND J38-103 103 G4_5VD J38-104 104 G4_DGND J38-105 105 G4_5VD J38-106 106 G4_DGND J38-107 107 G4_5VD J38-108 108 G4_DGND J38-109 109 G4_5VD J38-110 110 G4_DGND J38-111 111 G4_5VD J38-112 112 G4_DGND J38-113 113 G4_5VD J38-114 114 G4_DGND J38-115 115 G4_5VD J38-116 116 G4_DGND J38-117 117 G4_5VD J38-118 118 G4_DGND J38-119 119 G4_5VD J38-120 120 G4_DGND J38-121 121 G4_5VD J38-122 122 G4_DGND J38-123 123 G4_5VD J38-124 124 G4_DGND J38-125 125 G4_5VD J38-126 126 G4_DGND J38-127 127 G4_5VD J38-128 128 G4_DGND J38-129 129 G4_5VD J38-130 130 G4_DGND J38-131 131 G4_5VD J38-132 132 G4_DGND J38-133 133 G4_5VD J38-134 134 G4_DGND J38-135 135 G4_5VD J38-136 136 G4_DGND J38-137 137 G4_5VD J38-138 138 G4_DGND J38-139 139 G4_5VD J38-140 140 G4_DGND J38-141 141 G4_5VD J38-142 142 G4_DGND J38-143 143 G4_5VD J38-144 144 G4_DGND J38-145 145 G4_5VD J38-146 146 G4_DGND J38-147 147 G4_5VD J38-148 148 G4_DGND J38-149 149 G4_5VD J38-150 150 G4_DGND J38-151 151 G4_5VD J38-152 152 G4_DGND J38-153 153 G4_5VD J38-154 154 G4_DGND J38-155 155 G4_5VD J38-156 156 G4_DGND J38-157 157 G4_5VD J38-158 158 G4_DGND J38-159 159 G4_5VD J37-1 1 G5_MB0 J37-2 2 G5_MB1 J37-3 3 G5_MB2 J37-4 4 G5_MB3 J37-5 5 G5_MB4 J37-6 6 G5_MB5 J37-7 7 G5_DGND J37-8 8 G5_DGND J37-9 9 G5_L1A J37-10 10 G5_9.5MHZ_CLK J37-11 11 G5_DGND J37-12 12 G5_DGND J37-13 13 G5_PECCL-DIFF_CLKA J37-14 14 G5_PECCL-DIFF_CLKB J37-15 15 G5_MCM1_LVDS_DATA_OUT_CLKA J37-16 16 G5_MCM1_LVDS_DATA_OUT_CLKB J37-17 17 G5_MCM1_LVDS_DATA_OUT_1A J37-18 18 G5_MCM1_LVDS_DATA_OUT_1B J37-19 19 G5_MCM1_LVDS_DATA_OUT_2A J37-20 20 G5_MCM1_LVDS_DATA_OUT_2B J37-21 21 G5_MCM2_LVDS_DATA_OUT_1A J37-22 22 G5_MCM2_LVDS_DATA_OUT_1B J37-23 23 G5_MCM2_LVDS_DATA_OUT_2A J37-24 24 G5_MCM2_LVDS_DATA_OUT_2B J37-25 25 G5_MCM3_LVDS_DATA_OUT_1A J37-26 26 G5_MCM3_LVDS_DATA_OUT_1B J37-27 27 G5_MCM3_LVDS_DATA_OUT_2A J37-28 28 G5_MCM3_LVDS_DATA_OUT_2B J37-29 29 G5_MCM4_LVDS_DATA_OUT_1A J37-30 30 G5_MCM4_LVDS_DATA_OUT_1B J37-31 31 G5_MCM4_LVDS_DATA_OUT_2A J37-32 32 G5_MCM4_LVDS_DATA_OUT_2B J37-33 33 G5_MCM5_LVDS_DATA_OUT_1A J37-34 34 G5_MCM5_LVDS_DATA_OUT_1B J37-35 35 G5_MCM5_LVDS_DATA_OUT_2A J37-36 36 G5_MCM5_LVDS_DATA_OUT_2B J37-37 37 G5_MCM6_LVDS_DATA_OUT_1A J37-38 38 G5_MCM6_LVDS_DATA_OUT_1B J37-39 39 G5_MCM6_LVDS_DATA_OUT_2A J37-40 40 G5_MCM6_LVDS_DATA_OUT_2B J37-41 41 G5_DGND J37-42 42 G5_MCM1_S_EN J37-43 43 G5_MCM2_S_EN J37-44 44 G5_MCM3_S_EN J37-45 45 G5_MCM4_S_EN J37-46 46 G5_MCM5_S_EN J37-47 47 G5_DGND J37-48 48 G5_SERIAL_CLK J37-49 49 G5_DGND J37-50 50 G5_S_DATA_OUT_MCM1 J37-51 51 G5_S_DATA_OUT_MCM2 J37-52 52 G5_S_DATA_OUT_MCM3 J37-53 53 G5_S_DATA_OUT_MCM4 J37-54 54 G5_S_DATA_OUT_MCM5 J37-55 55 G5_DGND J37-56 56 G5_S_LATCH J37-57 57 G5_READBACK_EN J37-58 58 G5_XILINX_PROGRAM J37-59 59 PC5_TEMP_MON J37-60 60 G5_MCM1_PREAMP_OUT_SPY J37-61 61 G5_MCM2_PREAMP_OUT_SPY J37-62 62 G5_MCM3_PREAMP_OUT_SPY J37-63 63 G5_MCM4_PREAMP_OUT_SPY J37-64 64 G5_MCM5_PREAMP_OUT_SPY J37-65 65 G5_MCM6_PREAMP_OUT_SPY J37-66 66 MB_AGND J37-67 67 G5_MCM1_DS J37-68 68 G5_MCM2_DS J37-69 69 G5_MCM3_DS J37-70 70 G5_MCM4_DS J37-71 71 G5_MCM5_DS J37-72 72 G5_DGND J37-73 73 G5_5VD J37-74 74 G5_DGND J37-75 75 G5_5VD J37-76 76 G5_DGND J37-77 77 G5_5VD J37-78 78 G5_DGND J37-79 79 G5_5VD J37-80 80 G5_DGND J37-81 81 G5_5VD J37-82 82 G5_DGND J37-83 83 G5_5VD J37-84 84 G5_DGND J37-85 85 G5_5VD J37-86 86 G5_DGND J37-87 87 G5_5VD J37-88 88 G5_DGND J37-89 89 G5_5VD J37-90 90 G5_DGND J37-91 91 G5_5VD J37-92 92 G5_DGND J37-93 93 G5_5VD J37-94 94 G5_DGND J37-95 95 G5_5VD J37-96 96 G5_DGND J37-97 97 G5_5VD J37-98 98 G5_DGND J37-99 99 G5_5VD J37-100 100 G5_DGND J37-101 101 G5_5VD J37-102 102 G5_DGND J37-103 103 G5_5VD J37-104 104 G5_DGND J37-105 105 G5_5VD J37-106 106 G5_DGND J37-107 107 G5_5VD J37-108 108 G5_DGND J37-109 109 G5_5VD J37-110 110 G5_DGND J37-111 111 G5_5VD J37-112 112 G5_DGND J37-113 113 G5_5VD J37-114 114 G5_DGND J37-115 115 G5_5VD J37-116 116 G5_DGND J37-117 117 G5_5VD J37-118 118 G5_DGND J37-119 119 G5_5VD J37-120 120 G5_DGND J37-121 121 G5_5VD J37-122 122 G5_DGND J37-123 123 G5_5VD J37-124 124 G5_DGND J37-125 125 G5_5VD J37-126 126 G5_DGND J37-127 127 G5_5VD J37-128 128 G5_DGND J37-129 129 G5_5VD J37-130 130 G5_DGND J37-131 131 G5_5VD J37-132 132 G5_DGND J37-133 133 G5_5VD J37-134 134 G5_DGND J37-135 135 G5_5VD J37-136 136 G5_DGND J37-137 137 G5_5VD J37-138 138 G5_DGND J37-139 139 G5_5VD J37-140 140 G5_DGND J37-141 141 G5_5VD J37-142 142 G5_DGND J37-143 143 G5_5VD J37-144 144 G5_DGND J37-145 145 G5_5VD J37-146 146 G5_DGND J37-147 147 G5_5VD J37-148 148 G5_DGND J37-149 149 G5_5VD J37-150 150 G5_DGND J37-151 151 G5_5VD J37-152 152 G5_DGND J37-153 153 G5_5VD J37-154 154 G5_DGND J37-155 155 G5_5VD J37-156 156 G5_DGND J37-157 157 G5_5VD J37-158 158 G5_DGND J37-159 159 G5_5VD J36-1 1 G6_MB0 J36-2 2 G6_MB1 J36-3 3 G6_MB2 J36-4 4 G6_MB3 J36-5 5 G6_MB4 J36-6 6 G6_MB5 J36-7 7 G6_DGND J36-8 8 G6_DGND J36-9 9 G6_L1A J36-10 10 G6_9.5MHZ_CLK J36-11 11 G6_DGND J36-12 12 G6_DGND J36-13 13 G6_PECCL-DIFF_CLKA J36-14 14 G6_PECCL-DIFF_CLKB J36-15 15 G6_MCM1_LVDS_DATA_OUT_CLKA J36-16 16 G6_MCM1_LVDS_DATA_OUT_CLKB J36-17 17 G6_MCM1_LVDS_DATA_OUT_1A J36-18 18 G6_MCM1_LVDS_DATA_OUT_1B J36-19 19 G6_MCM1_LVDS_DATA_OUT_2A J36-20 20 G6_MCM1_LVDS_DATA_OUT_2B J36-21 21 G6_MCM2_LVDS_DATA_OUT_1A J36-22 22 G6_MCM2_LVDS_DATA_OUT_1B J36-23 23 G6_MCM2_LVDS_DATA_OUT_2A J36-24 24 G6_MCM2_LVDS_DATA_OUT_2B J36-25 25 G6_MCM3_LVDS_DATA_OUT_1A J36-26 26 G6_MCM3_LVDS_DATA_OUT_1B J36-27 27 G6_MCM3_LVDS_DATA_OUT_2A J36-28 28 G6_MCM3_LVDS_DATA_OUT_2B J36-29 29 G6_MCM4_LVDS_DATA_OUT_1A J36-30 30 G6_MCM4_LVDS_DATA_OUT_1B J36-31 31 G6_MCM4_LVDS_DATA_OUT_2A J36-32 32 G6_MCM4_LVDS_DATA_OUT_2B J36-33 33 G6_MCM5_LVDS_DATA_OUT_1A J36-34 34 G6_MCM5_LVDS_DATA_OUT_1B J36-35 35 G6_MCM5_LVDS_DATA_OUT_2A J36-36 36 G6_MCM5_LVDS_DATA_OUT_2B J36-37 37 G6_MCM6_LVDS_DATA_OUT_1A J36-38 38 G6_MCM6_LVDS_DATA_OUT_1B J36-39 39 G6_MCM6_LVDS_DATA_OUT_2A J36-40 40 G6_MCM6_LVDS_DATA_OUT_2B J36-41 41 G6_DGND J36-42 42 G6_MCM2_S_EN J36-43 43 G6_MCM3_S_EN J36-44 44 G6_MCM4_S_EN J36-45 45 G6_MCM5_S_EN J36-46 46 G6_DGND J36-47 47 G6_SERIAL_CLK J36-48 48 G6_DGND J36-49 49 G6_S_DATA_OUT_MCM1 J36-50 50 G6_S_DATA_OUT_MCM2 J36-51 51 G6_S_DATA_OUT_MCM3 J36-52 52 G6_S_DATA_OUT_MCM4 J36-53 53 G6_S_DATA_OUT_MCM5 J36-54 54 G6_DGND J36-55 55 G6_S_LATCH J36-56 56 G6_READBACK_EN J36-57 57 G6_XILINX_PROGRAM J36-58 58 PC6_TEMP_MON J36-59 59 G6_MCM1_PREAMP_OUT_SPY J36-60 60 G6_MCM2_PREAMP_OUT_SPY J36-61 61 G6_MCM3_PREAMP_OUT_SPY J36-62 62 G6_MCM4_PREAMP_OUT_SPY J36-63 63 G6_MCM5_PREAMP_OUT_SPY J36-64 64 G6_MCM6_PREAMP_OUT_SPY J36-65 65 MB_AGND J36-66 66 G6_MCM1_DS J36-67 67 G6_MCM2_DS J36-68 68 G6_MCM3_DS J36-69 69 G6_MCM4_DS J36-70 70 G6_MCM5_DS J36-71 71 G6_DGND J36-72 72 G6_5VD J36-73 73 G6_DGND J36-74 74 G6_5VD J36-75 75 G6_DGND J36-76 76 G6_5VD J36-77 77 G6_DGND J36-78 78 G6_5VD J36-79 79 G6_DGND J36-80 80 G6_5VD J36-81 81 G6_DGND J36-82 82 G6_5VD J36-83 83 G6_DGND J36-84 84 G6_5VD J36-85 85 G6_DGND J36-86 86 G6_5VD J36-87 87 G6_DGND J36-88 88 G6_5VD J36-89 89 G6_DGND J36-90 90 G6_5VD J36-91 91 G6_DGND J36-92 92 G6_5VD J36-93 93 G6_DGND J36-94 94 G6_5VD J36-95 95 G6_DGND J36-96 96 G6_5VD J36-97 97 G6_DGND J36-98 98 G6_5VD J36-99 99 G6_DGND J36-100 100 G6_5VD J36-101 101 G6_DGND J36-102 102 G6_5VD J36-103 103 G6_DGND J36-104 104 G6_5VD J36-105 105 G6_DGND J36-106 106 G6_5VD J36-107 107 G6_DGND J36-108 108 G6_5VD J36-109 109 G6_DGND J36-110 110 G6_5VD							