

	1	2	3	4	5	6	7	8
	MOTHERBOARD CONNECTOR GROUP1		MOTHERBOARD CONNECTOR GROUP2		MOTHERBOARD CONNECTOR GROUP3			
A	J41-1 G1_MB0 1 J41-3 G1_MB2 3 J41-5 G1_MB4 5 J41-7 G1_DGND 7 J41-9 G1_L1A 9 J41-11 G1_DGND 11 J41-13 G1_PECL_DIFF_CLKA 13 J41-15 G1_PECL_DIFF_CLKB 15 J41-17 G1_MCM1_LVDS_DATA_OUT_CLKA 17 J41-19 G1_MCM1_LVDS_DATA_OUT_CLKB 19 J41-21 G1_MCM1_LVDS_DATA_OUT_2A 21 J41-23 G1_MCM1_LVDS_DATA_OUT_2B 23 J41-25 G1_MCM2_LVDS_DATA_OUT_1A 25 J41-27 G1_MCM2_LVDS_DATA_OUT_1B 27 J41-29 G1_MCM3_LVDS_DATA_OUT_CLKA 29 J41-31 G1_MCM3_LVDS_DATA_OUT_CLKB 31 J41-33 G1_MCM3_LVDS_DATA_OUT_2A 33 J41-35 G1_MCM3_LVDS_DATA_OUT_2B 35 J41-37 G1_MCM4_LVDS_DATA_OUT_1A 37 J41-39 G1_MCM4_LVDS_DATA_OUT_1B 39 J41-41 G1_MCM5_LVDS_DATA_OUT_CLKA 41 J41-43 G1_MCM5_LVDS_DATA_OUT_CLKB 43 J41-45 G1_MCM5_LVDS_DATA_OUT_2A 45 J41-47 G1_MCM5_LVDS_DATA_OUT_2B 47 J41-49 G1_MCM6_LVDS_DATA_OUT_1A 49 J41-51 G1_MCM6_LVDS_DATA_OUT_1B 51 J41-53 G1_DGND 53 J41-55 G1_MCM1_S_EN 55 J41-57 G1_MCM3_S_EN 57 J41-59 G1_MCM5_S_EN 59 J41-61 G1_DGND 61 J41-63 G1_SERIAL_CLK 63 J41-65 G1_DGND 65 J41-67 G1_S_DATA_OUT_MCM1 67 J41-69 G1_S_DATA_OUT_MCM3 69 J41-71 G1_S_DATA_OUT_MCM5 71 J41-73 G1_DGND 73 J41-75 G1_S_LATCH 75 J41-77 G1_XILINX_PROGRAM 77 J41-79 PC1_TEMP_MON 79 J41-81 G1_MCM1_PREAMP_OUT_SPY 81 J41-83 G1_MCM2_PREAMP_OUT_SPY 83 J41-85 G1_MCM3_PREAMP_OUT_SPY 85 J41-87 G1_MCM4_PREAMP_OUT_SPY 87 J41-89 G1_MCM5_PREAMP_OUT_SPY 89 J41-91 G1_MCM6_PREAMP_OUT_SPY 91 J41-93 MB_AGND 93 J41-95 G1_MCM1_DS 95 J41-97 G1_MCM3_DS 97 J41-99 G1_MCM5_DS 99 J41-101 G1_DGND 101 J41-103 G1_5VD 103 J41-105 G1_DGND 105 J41-107 G1_5VD 107 J41-109 G1_DGND 109 J41-111 G1_5VD 111 J41-113 G1_DGND 113 J41-115 G1_5VD 115 J41-117 G1_DGND 117 J41-119 G1_5VD 119 J41-121 G1_DGND 121 J41-123 G1_5VD 123 J41-125 G1_DGND 125 J41-127 G1_5VD 127 J41-129 G1_DGND 129 J41-131 G1_5VD 131 J41-133 G1_DGND 133 J41-135 G1_5VD 135 J41-137 G1_DGND 137 J41-139 G1_5VD 139 J41-141 G1_DGND 141 J41-143 G1_5VD 143 J41-145 G1_MCM3_TEMP_MON 145 J41-147 G1_MCM5_TEMP_MON 147 J41-149 G1_MCM1_SBR 149 J41-151 G1_MCM2_SBR 151 J41-153 G1_MCM3_SBR 153 J41-155 G1_MCM4_SBR 155 J41-157 G1_MCM5_SBR 157 J41-159 G1_MCM6_SBR 159 J41-2 G1_MB1 2 J41-4 G1_MB3 4 J41-6 G1_MB5 6 J41-8 G1_DGND 8 J41-10 G1_9.5MHZ_CLK 10 J41-12 G1_DGND 12 J41-14 G1_38MHZ_CLK 14 J41-16 G1_DGND 16 J41-18 G1_MCM1_LVDS_DATA_OUT_1A 18 J41-20 G1_MCM1_LVDS_DATA_OUT_1B 20 J41-22 G1_MCM2_LVDS_DATA_OUT_CLKA 22 J41-24 G1_MCM2_LVDS_DATA_OUT_CLKB 24 J41-26 G1_MCM2_LVDS_DATA_OUT_2A 26 J41-28 G1_MCM2_LVDS_DATA_OUT_2B 28 J41-30 G1_MCM3_LVDS_DATA_OUT_1A 30 J41-32 G1_MCM3_LVDS_DATA_OUT_1B 32 J41-34 G1_MCM4_LVDS_DATA_OUT_CLKA 34 J41-36 G1_MCM4_LVDS_DATA_OUT_CLKB 36 J41-38 G1_MCM4_LVDS_DATA_OUT_2A 38 J41-40 G1_MCM4_LVDS_DATA_OUT_2B 40 J41-42 G1_MCM5_LVDS_DATA_OUT_1A 42 J41-44 G1_MCM5_LVDS_DATA_OUT_1B 44 J41-46 G1_MCM6_LVDS_DATA_OUT_CLKA 46 J41-48 G1_MCM6_LVDS_DATA_OUT_CLKB 48 J41-50 G1_MCM6_LVDS_DATA_OUT_2A 50 J41-52 G1_MCM6_LVDS_DATA_OUT_2B 52 J41-54 G1_DGND 54 J41-56 G1_MCM2_S_EN 56 J41-58 G1_MCM4_S_EN 58 J41-60 G1_MCM6_S_EN 60 J41-62 G1_DGND 62 J41-64 G1_S_DATA_IN 64 J41-66 G1_DGND 66 J41-68 G1_S_DATA_OUT_MCM2 68 J41-70 G1_S_DATA_OUT_MCM4 70 J41-72 G1_S_DATA_OUT_MCM6 72 J41-74 G1_DGND 74 J41-76 G1_READBACK_EN 76 J41-78 G1_MCM_RESET 78 J41-80 MB_AGND 80 J41-82 G1_MCM1_AMU_OUT_SPY 82 J41-84 G1_MCM2_AMU_OUT_SPY 84 J41-86 G1_MCM3_AMU_OUT_SPY 86 J41-88 G1_MCM4_AMU_OUT_SPY 88 J41-90 G1_MCM5_AMU_OUT_SPY 90 J41-92 G1_MCM6_AMU_OUT_SPY 92 J41-94 MB_AGND 94 J41-96 G1_MCM1_DS 96 J41-98 G1_MCM4_DS 98 J41-100 G1_MCM6_DS 100 J41-102 G1_DGND 102 J41-104 G1_5VD 104 J41-106 G1_AGND_COMP 106 J41-108 G1_5VA_COMP 108 J41-110 G1_AGND_COMP 110 J41-112 G1_5VA_COMP 112 J41-114 G1_AGND_TGV 114 J41-116 G1_5VA_TGV 116 J41-118 G1_AGND_TGV 118 J41-120 G1_5VA_TGV 120 J41-122 G1_DGND 122 J41-124 G1_5VA_TGV 124 J41-126 G1_AGND_TGV 126 J41-128 G1_5VA_TGV 128 J41-130 G1_-5VA 130 J41-132 G1_-5VA 132 J41-134 G1_-5VA 134 J41-136 G1_2_5VA 136 J41-138 G1_2_5VA 138 J41-140 G1_2_5VA 140 J41-142 G1_MCM1_TEMP_MON 142 J41-144 G1_MCM2_TEMP_MON 144 J41-146 G1_MCM4_TEMP_MON 146 J41-148 G1_MCM6_TEMP_MON 148 J41-150 G1_MCM1_SBR 150 J41-152 G1_MCM2_SBR 152 J41-154 G1_MCM3_SBR 154 J41-156 G1_MCM4_SBR 156 J41-158 G1_MCM5_SBR 158 J41-160 G1_MCM6_SBR 160 J40-1 G2_MB0 1 J40-3 G2_MB2 3 J40-5 G2_MB4 5 J40-7 G2_DGND 7 J40-9 G2_L1A 9 J40-11 G2_DGND 11 J40-13 G2_PECL_DIFF_CLKA 13 J40-15 G2_PECL_DIFF_CLKB 15 J40-17 G2_MCM1_LVDS_DATA_OUT_CLKA 17 J40-19 G2_MCM1_LVDS_DATA_OUT_CLKB 19 J40-21 G2_MCM1_LVDS_DATA_OUT_2A 21 J40-23 G2_MCM1_LVDS_DATA_OUT_2B 23 J40-25 G2_MCM2_LVDS_DATA_OUT_1A 25 J40-27 G2_MCM2_LVDS_DATA_OUT_1B 27 J40-29 G2_MCM3_LVDS_DATA_OUT_CLKA 29 J40-31 G2_MCM3_LVDS_DATA_OUT_CLKB 31 J40-33 G2_MCM3_LVDS_DATA_OUT_2A 33 J40-35 G2_MCM3_LVDS_DATA_OUT_2B 35 J40-37 G2_MCM4_LVDS_DATA_OUT_1A 37 J40-39 G2_MCM4_LVDS_DATA_OUT_1B 39 J40-41 G2_MCM5_LVDS_DATA_OUT_CLKA 41 J40-43 G2_MCM5_LVDS_DATA_OUT_CLKB 43 J40-45 G2_MCM5_LVDS_DATA_OUT_2A 45 J40-47 G2_MCM5_LVDS_DATA_OUT_2B 47 J40-49 G2_MCM6_LVDS_DATA_OUT_1A 49 J40-51 G2_MCM6_LVDS_DATA_OUT_1B 51 J40-53 G2_DGND 53 J40-55 G2_MCM1_S_EN 55 J40-57 G2_MCM3_S_EN 57 J40-59 G2_MCM5_S_EN 59 J40-61 G2_DGND 61 J40-63 G2_SERIAL_CLK 63 J40-65 G2_DGND 65 J40-67 G2_S_DATA_OUT_MCM1 67 J40-69 G2_S_DATA_OUT_MCM3 69 J40-71 G2_S_DATA_OUT_MCM5 71 J40-73 G2_DGND 73 J40-75 G2_S_LATCH 75 J40-77 G2_XILINX_PROGRAM 77 J40-79 PC2_TEMP_MON 79 J40-81 G2_MCM1_PREAMP_OUT_SPY 81 J40-83 G2_MCM2_PREAMP_OUT_SPY 83 J40-85 G2_MCM3_PREAMP_OUT_SPY 85 J40-87 G2_MCM4_PREAMP_OUT_SPY 87 J40-89 G2_MCM5_PREAMP_OUT_SPY 89 J40-91 G2_MCM6_PREAMP_OUT_SPY 91 J40-93 MB_AGND 93 J40-95 G2_MCM1_DS 95 J40-97 G2_MCM3_DS 97 J40-99 G2_MCM5_DS 99 J40-101 G2_DGND 101 J40-103 G2_5VD 103 J40-105 G2_DGND 105 J40-107 G2_5VD 107 J40-109 G2_DGND 109 J40-111 G2_5VD 111 J40-113 G2_DGND 113 J40-115 G2_5VD 115 J40-117 G2_DGND 117 J40-119 G2_5VD 119 J40-121 G2_DGND 121 J40-123 G2_5VD 123 J40-125 G2_DGND 125 J40-127 G2_5VD 127 J40-129 G2_DGND 129 J40-131 G2_5VD 131 J40-133 G2_DGND 133 J40-135 G2_5VD 135 J40-137 G2_DGND 137 J40-139 G2_2_5VA 139 J40-141 G2_2_5VA 1							