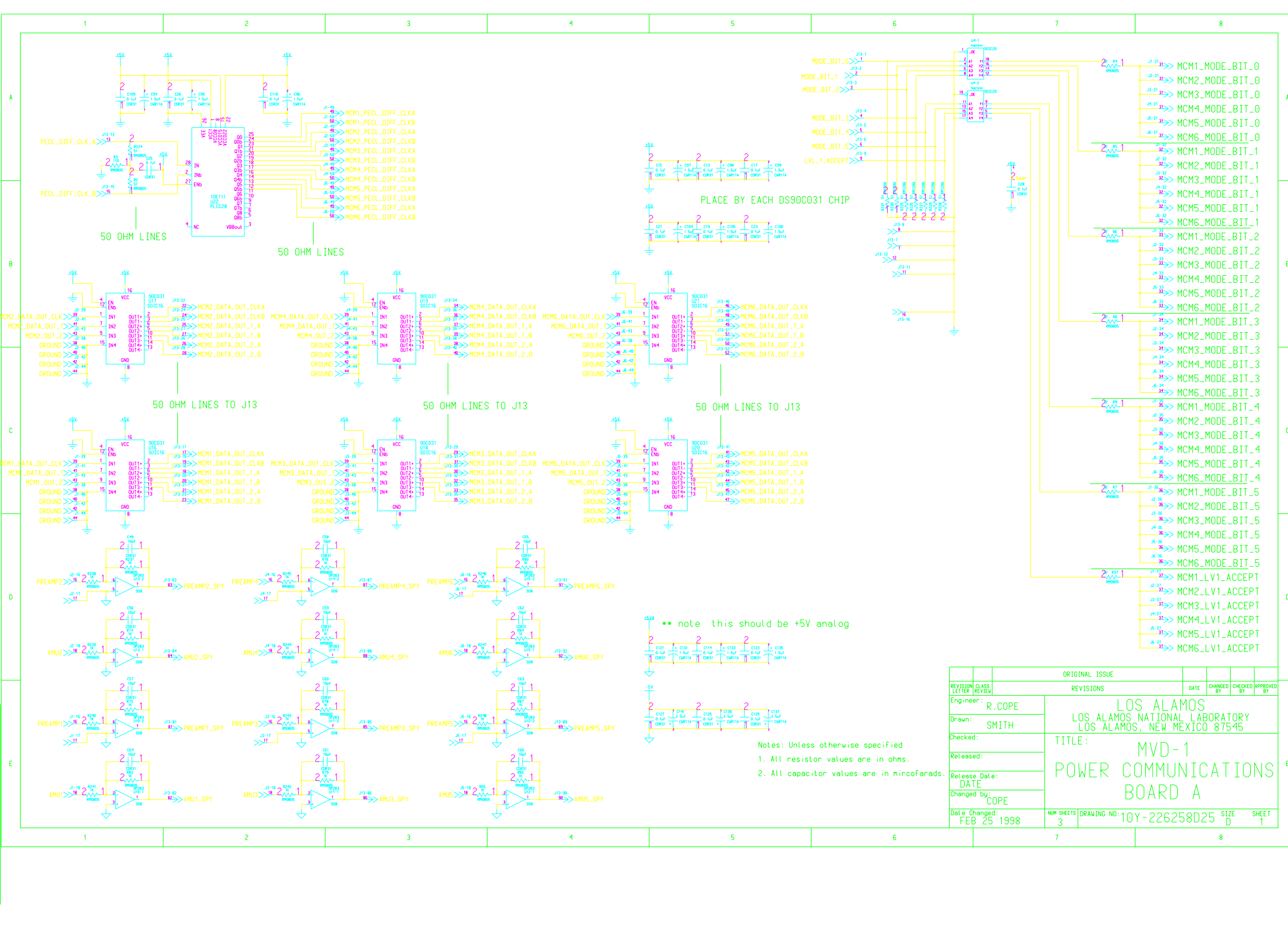
PECL_DIFF_CLK_A
PECL_DIFF_CLK_B
50 OHM LINES
50 OHM LINES
MCM1_PECL_DIFF_CLKA
MCM1_PECL_DIFF_CLKB
MCM2_PECL_DIFF_CLKA
MCM2_PECL_DIFF_CLKB
MCM3_PECL_DIFF_CLKA
MCM3_PECL_DIFF_CLKB
MCM4_PECL_DIFF_CLKA
MCM4_PECL_DIFF_CLKB
MCM5_PECL_DIFF_CLKA
MCM5_PECL_DIFF_CLKB
MCM6_PECL_DIFF_CLKA
MCM6_PECL_DIFF_CLKB
MODE_BIT_0
MODE_BIT_1
MODE_BIT_2
MODE_BIT_3
MODE_BIT_4
MODE_BIT_5
LVL_1_ACCEPT
PLACE BY EACH DS90C031 CHIP
MCM1_MODE_BIT_0
MCM2_MODE_BIT_0
MCM3_MODE_BIT_0
MCM4_MODE_BIT_0
MCM5_MODE_BIT_0
MCM6_MODE_BIT_0
MCM1_MODE_BIT_1
MCM2_MODE_BIT_1
MCM3_MODE_BIT_1
MCM4_MODE_BIT_1
MCM5_MODE_BIT_1
MCM6_MODE_BIT_1
MCM1_MODE_BIT_2
MCM2_MODE_BIT_2
MCM3_MODE_BIT_2
MCM4_MODE_BIT_2
MCM5_MODE_BIT_2
MCM6_MODE_BIT_2
MCM1_MODE_BIT_3
MCM2_MODE_BIT_3
MCM3_MODE_BIT_3
MCM4_MODE_BIT_3
MCM5_MODE_BIT_3
MCM6_MODE_BIT_3
MCM1_MODE_BIT_4
MCM2_MODE_BIT_4
MCM3_MODE_BIT_4
MCM4_MODE_BIT_4
MCM5_MODE_BIT_4
MCM6_MODE_BIT_4
MCM1_MODE_BIT_5
MCM2_MODE_BIT_5
MCM3_MODE_BIT_5
MCM4_MODE_BIT_5
MCM5_MODE_BIT_5
MCM6_MODE_BIT_5
MCM1_LV1_ACCEPT
MCM2_LV1_ACCEPT
MCM3_LV1_ACCEPT
MCM4_LV1_ACCEPT
MCM5_LV1_ACCEPT
MCM6_LV1_ACCEPT
MCM2_DATA_OUT_CLK
MCM2_DATA_OUT_1
MCM2_OUT_2
MCM2_DATA_OUT_CLKA
MCM2_DATA_OUT_CLKB
MCM2_DATA_OUT_1_A
MCM2_DATA_OUT_1_B
MCM2_DATA_OUT_2_A
MCM2_DATA_OUT_2_B
50 OHM LINES TO J13
MCM4_DATA_OUT_CLK
MCM4_DATA_OUT_1
MCM4_OUT_2
MCM4_DATA_OUT_CLKA
MCM4_DATA_OUT_CLKB
MCM4_DATA_OUT_1_A
MCM4_DATA_OUT_1_B
MCM4_DATA_OUT_2_A
MCM4_DATA_OUT_2_B
50 OHM LINES TO J13
MCM6_DATA_OUT_CLK
MCM6_DATA_OUT_1
MCM6_OUT_2
MCM6_DATA_OUT_CLKA
MCM6_DATA_OUT_CLKB
MCM6_DATA_OUT_1_A
MCM6_DATA_OUT_1_B
MCM6_DATA_OUT_2_A
MCM6_DATA_OUT_2_B
50 OHM LINES TO J13
MCM1_DATA_OUT_CLK
MCM1_DATA_OUT_1
MCM1_OUT_2
MCM1_DATA_OUT_CLKA
MCM1_DATA_OUT_CLKB
MCM1_DATA_OUT_1_A
MCM1_DATA_OUT_1_B
MCM1_DATA_OUT_2_A
MCM1_DATA_OUT_2_B
MCM3_DATA_OUT_CLK
MCM3_DATA_OUT_1
MCM3_OUT_2
MCM3_DATA_OUT_CLKA
MCM3_DATA_OUT_CLKB
MCM3_DATA_OUT_1_A
MCM3_DATA_OUT_1_B
MCM3_DATA_OUT_2_A
MCM3_DATA_OUT_2_B
MCM5_DATA_OUT_CLK
MCM5_DATA_OUT_1
MCM5_OUT_2
MCM5_DATA_OUT_CLKA
MCM5_DATA_OUT_CLKB
MCM5_DATA_OUT_1_A
MCM5_DATA_OUT_1_B
MCM5_DATA_OUT_2_A
MCM5_DATA_OUT_2_B
PREAMP2
PREAMP2_SPY
PREAMP4
PREAMP4_SPY
PREAMP6
PREAMP6_SPY
AMU2
AMU2_SPY
AMU4
AMU4_SPY
AMU6
AMU6_SPY
PREAMP1
PREAMP1_SPY
PREAMP3
PREAMP3_SPY
PREAMP5
PREAMP5_SPY
AMU1
AMU1_SPY
AMU3
AMU3_SPY
AMU5
AMU5_SPY
** note this should be +5V analog
Notes: Unless otherwise specified
1. All resistor values are in ohms.
2. All capacitor values are in mircofarads.
ORIGINAL ISSUE
REVISION LETTER
CLASS REVIEW
REVISIONS
DATE
CHANGED BY
CHECKED BY
APPROVED BY
Engineer: R.COPE
Drawn: SMITH
Checked:
Released:
Release Date: DATE
Changed by: COPE
Date Changed: FEB 25 1998
LOS ALAMOS
LOS ALAMOS NATIONAL LABORATORY
LOS ALAMOS, NEW MEXICO 87545
TITLE:
MVD-1
POWER COMMUNICATIONS
BOARD A
NUM SHEETS 3
DRAWING NO: 10Y-226258D25
SIZE D
SHEET 1