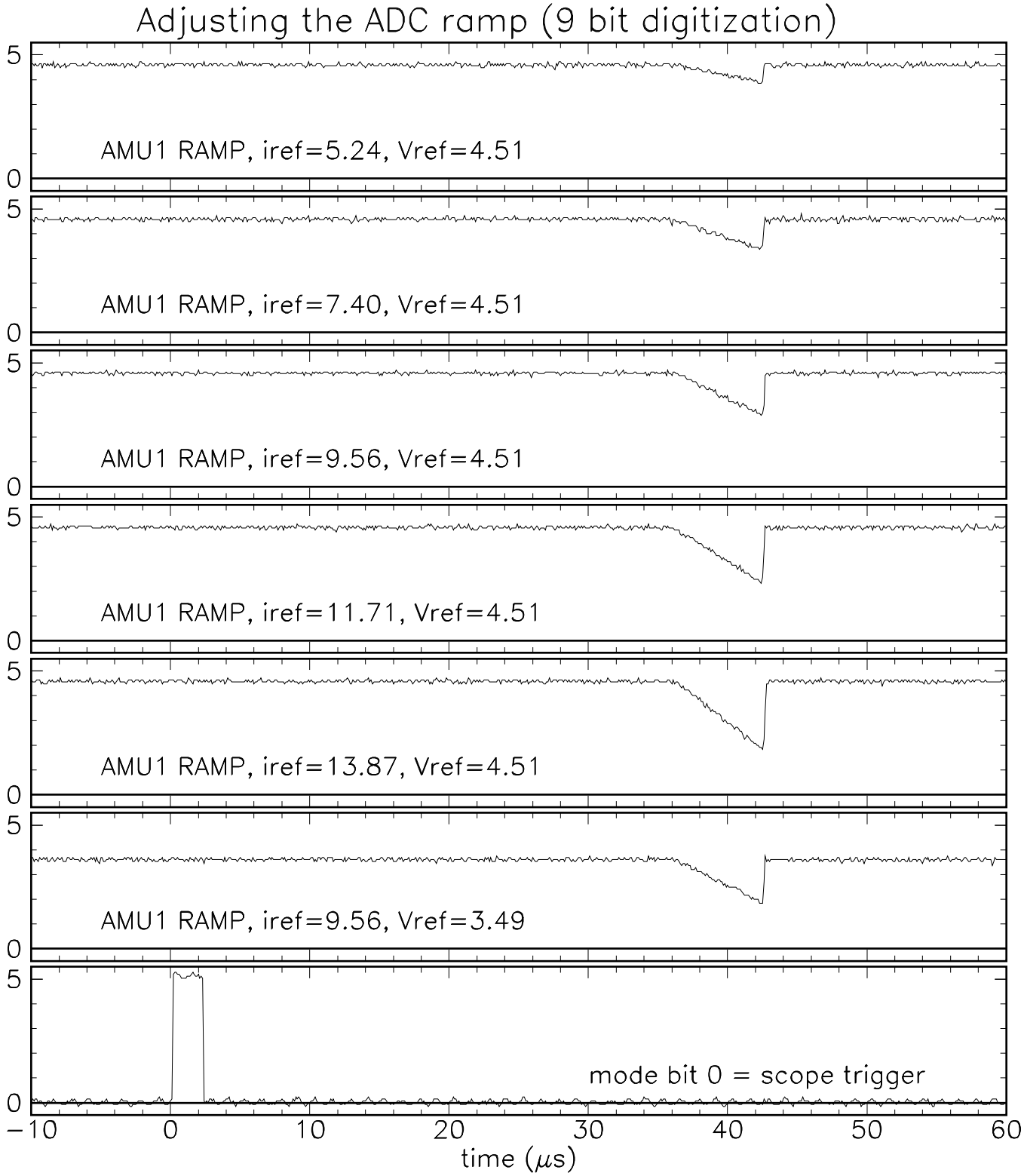
Adjusting the ADC ramp (9 bit digitization)
5
0
AMU1 RAMP, iref=5.24, Vref=4.51
AMU1 RAMP, iref=7.40, Vref=4.51
AMU1 RAMP, iref=9.56, Vref=4.51
AMU1 RAMP, iref=11.71, Vref=4.51
AMU1 RAMP, iref=13.87, Vref=4.51
AMU1 RAMP, iref=9.56, Vref=3.49
mode bit 0 = scope trigger
−10
0
10
20
30
40
50
60
time (μs)