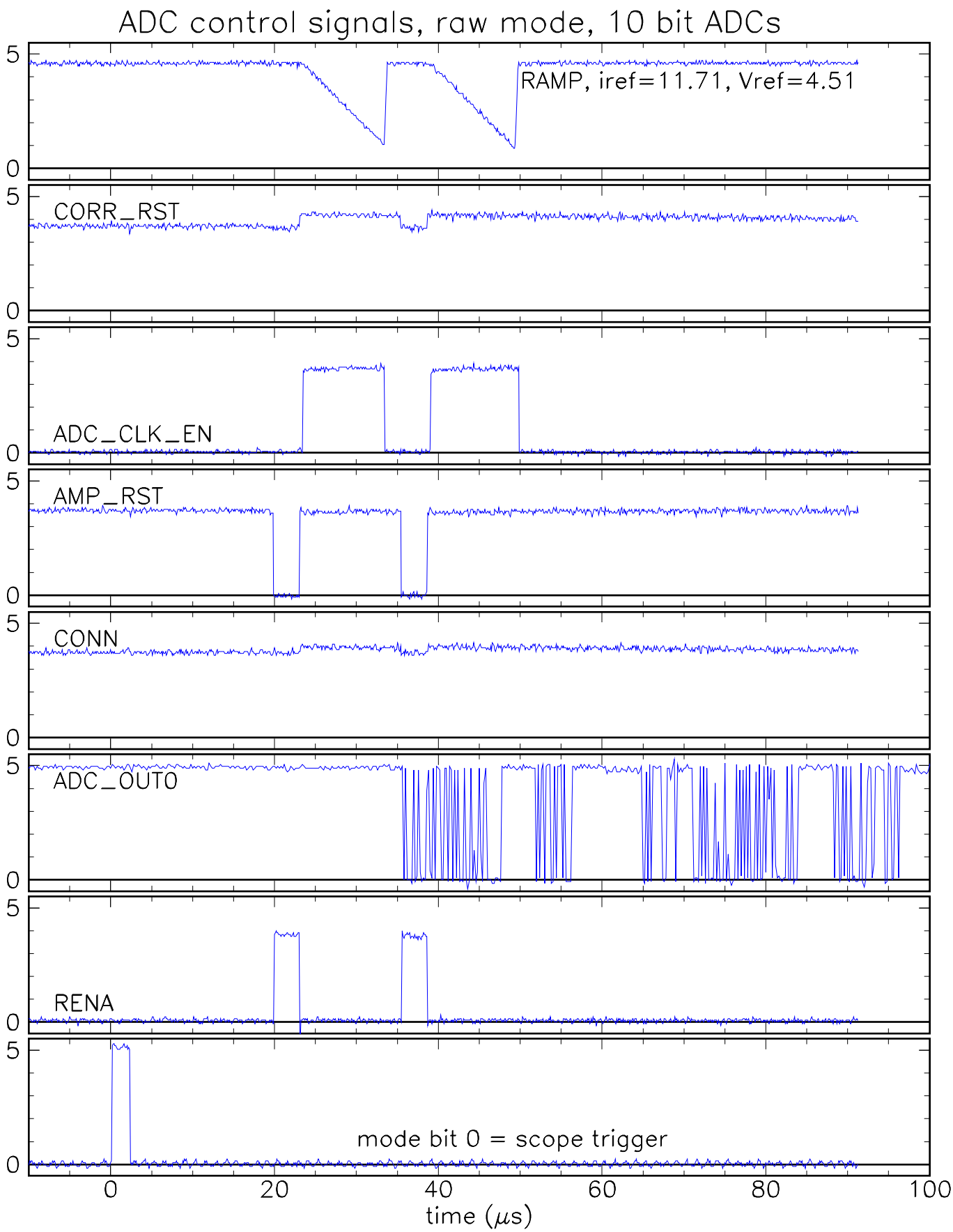
ADC control signals, raw mode, 10 bit ADCs
RAMP, iref=11.71, Vref=4.51
CORR_RST
ADC_CLK_EN
AMP_RST
CONN
ADC_OUT0
RENA
mode bit 0 = scope trigger
5
0
0
20
40
60
80
100
time (μs)