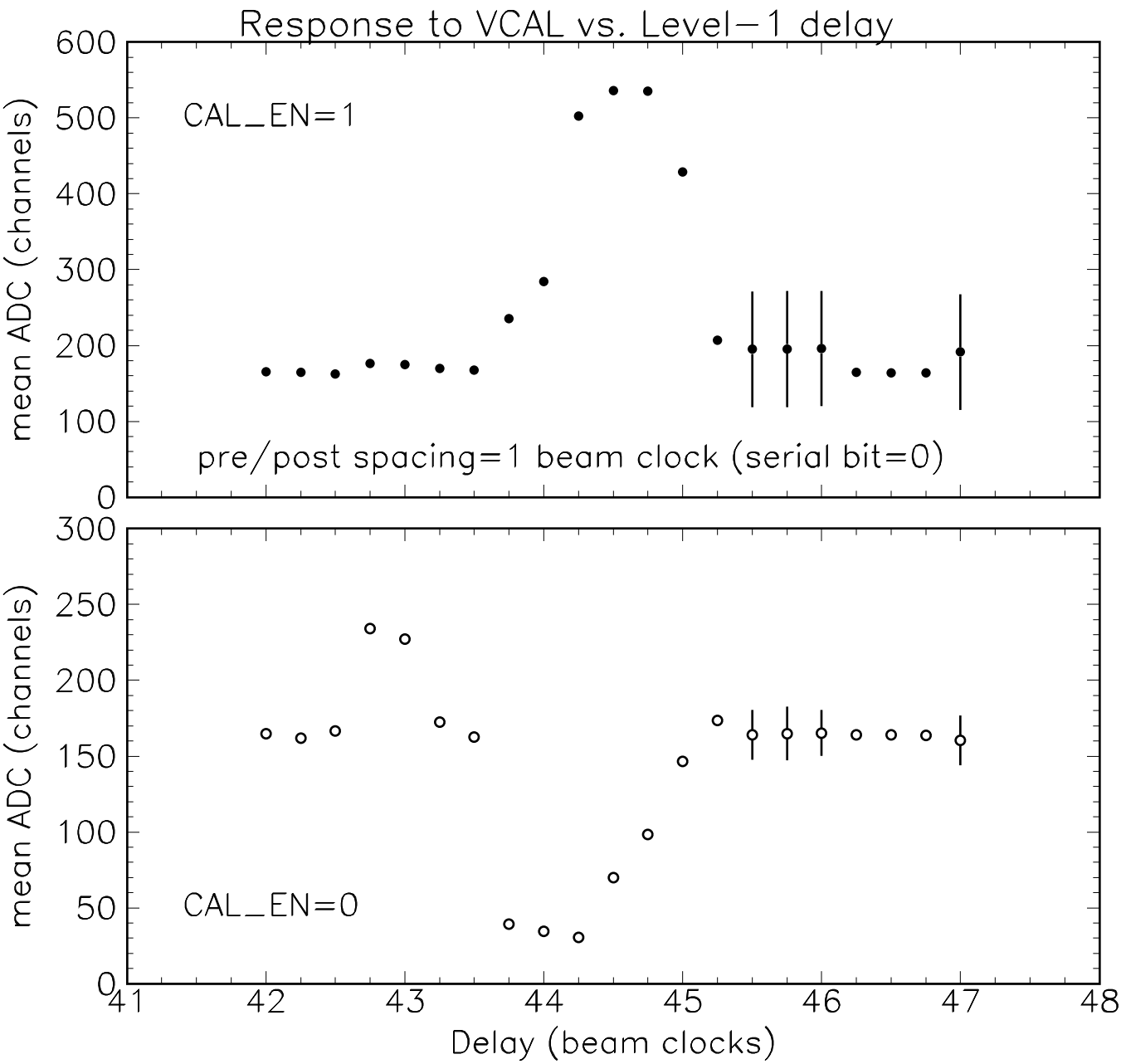
99/08/02 14.12


Response to VCAL vs. Level−1 delay
CAL_EN=1
pre/post spacing=1 beam clock (serial bit=0)
CAL_EN=0
mean ADC (channels)
mean ADC (channels)
Delay (beam clocks)
0
100
200
300
400
500
600
0
50
100
150
200
250
300
41
42
43
44
45
46
47
48