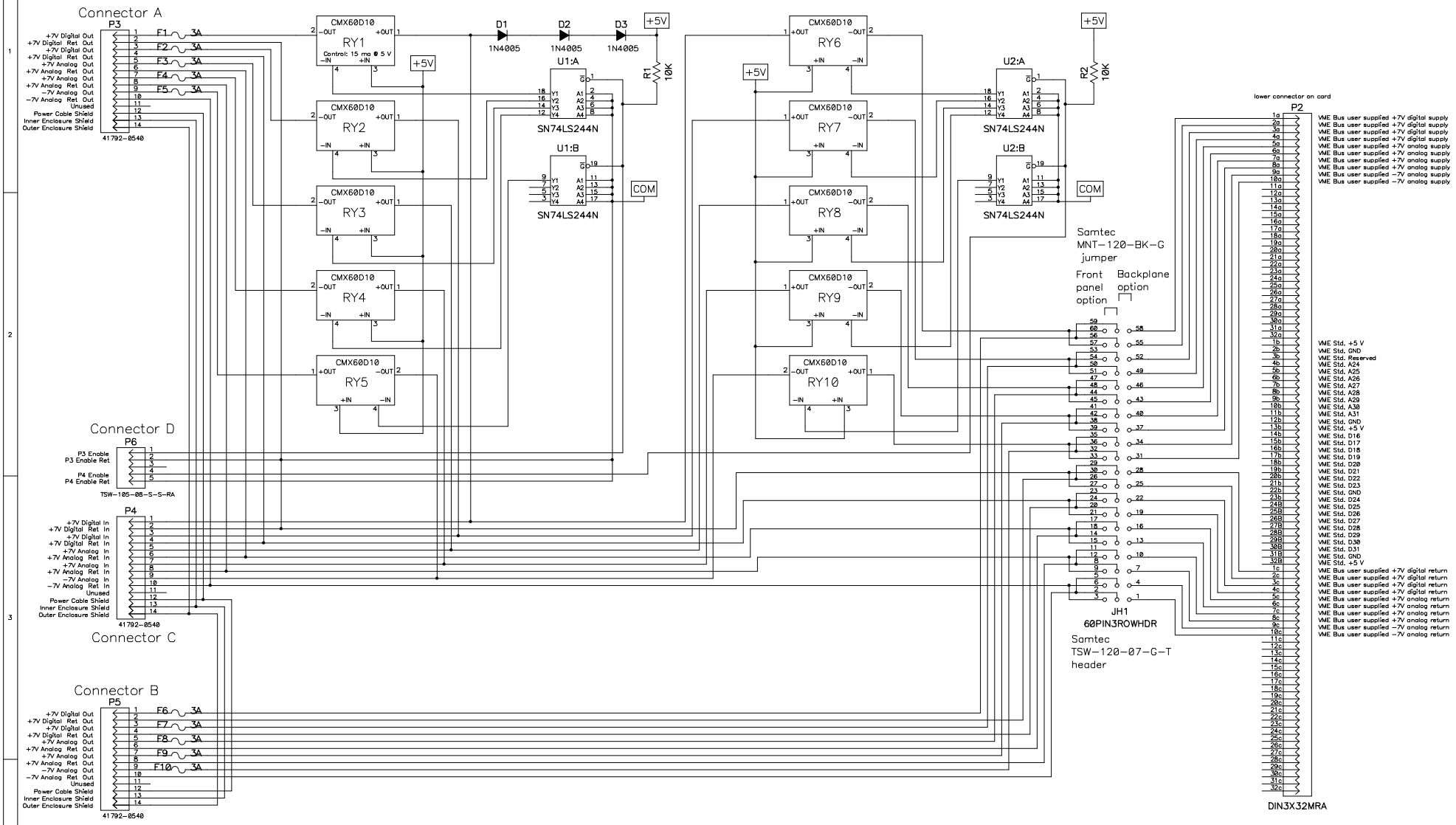

P3 Logic Table		
P3 Output	Open	P4 Power In
P6 Enable (pins 1 & 2)	Open	Closed

P5 Logic Table			
P5 Output	Open	P4 Power In	VME P2 Power
P6 Enable (pins 1 & 2)	Open	Closed	Either
Jumper position	Front Panel	Front Panel	Rear Panel

Maximum Component Height for VME cards = .540"

Los Alamos National Laboratory		
Title:	MVD LV Dist. VME Module	
Size	Date Originated: 17 May 1999	
	Last Revised: 4 Aug 1999	
LANL DWG #: MVD LV DIST		Approved By:Boissevain
PHENIX #: 002-0301-100		Drawn by: G. Hart P-25
Filename: MVD LV DIST.sch		Sheet of