



lower connector on card

1a	WE Bus user supplied +7V digital supply
2a	WE Bus user supplied +7V digital supply
3a	WE Bus user supplied +7V digital supply
4a	WE Bus user supplied +7V digital supply
5a	WE Bus user supplied +7V digital supply
6a	WE Bus user supplied +7V analog supply
7a	WE Bus user supplied +7V analog supply
8a	WE Bus user supplied +7V analog supply
9a	WE Bus user supplied +7V analog supply
10a	WE Bus user supplied +7V analog supply
11a	WE Bus user supplied +7V analog supply
12a	WE Bus user supplied +7V analog supply
13a	WE Bus user supplied +7V analog supply
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16a	WE Bus user supplied +7V analog supply
17a	WE Bus user supplied +7V analog supply
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49a	WE Bus user supplied +7V analog supply
50a	WE Bus user supplied +7V analog supply
51a	WE Bus user supplied +7V analog supply
52a	WE Bus user supplied +7V analog supply
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57a	WE Bus user supplied +7V analog supply
58a	WE Bus user supplied +7V analog supply
59a	WE Bus user supplied +7V analog supply
60a	WE Bus user supplied +7V analog supply
1a	WE Std. +5 V
2a	WE Std. GND
3a	WE Std. Reserved
4a	WE Std. A24
5a	WE Std. A25
6a	WE Std. A26
7a	WE Std. A27
8a	WE Std. A28
9a	WE Std. A29
10a	WE Std. A30
11a	WE Std. A31
12a	WE Std. GND
13a	WE Std. +5 V
14a	WE Std. D16
15a	WE Std. D17
16a	WE Std. D18
17a	WE Std. D19
18a	WE Std. D20
19a	WE Std. D21
20a	WE Std. D22
21a	WE Std. D23
22a	WE Std. GND
23a	WE Std. D24
24a	WE Std. D25
25a	WE Std. D26
26a	WE Std. D27
27a	WE Std. D28
28a	WE Std. D29
29a	WE Std. D30
30a	WE Std. D31
31a	WE Std. GND
32a	WE Std. +5 V
33a	WE Bus user supplied +7V digital return
34a	WE Bus user supplied +7V digital return
35a	WE Bus user supplied +7V digital return
36a	WE Bus user supplied +7V analog return
37a	WE Bus user supplied +7V analog return
38a	WE Bus user supplied +7V analog return
39a	WE Bus user supplied +7V analog return
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60a	WE Bus user supplied +7V analog return

Los Alamos National Laboratory	
Title:	MVD LV Dist. VME Module
Size	Date Originated: 17 May 1999
	Last Revised: 4 Aug 1999
LANL DWG #:	MVD LV DIST
PHENIX #:	002-0301-100
Filename:	MVD LV DIST.sch
Approved By:	Boissevain
Drawn by:	G. Hart P-25
Sheet	of

P3 Logic Table

P3 Output	Open	P4 Power In
P6 Enable (pins 1 & 2)	Open	Closed

P5 Logic Table

P5 Output	Open	P4 Power In	VME P2 Power
P6 Enable (pins 1 & 2)	Open	Closed	Either
Jumper position	Front Panel	Front Panel	Rear Panel

Maximum Component Height for VME cards = .540"