SILICON DETECTOR PARAMETERS

SILICON STRIPS -

outer layer - 0.05 mm x 71.5 mm, 0.2 mm pitch

kapton routers - 63, 90, 174 mm

single channel capacitance - 6 to 10 pF

inner layer - 0.05 mm x 49 mm, 0.2 mm pitch

kapton routers - 88, 140, 191 mm

single channel capacitance - 6 to 12 pF

SILICON PADS -

innermost radius - 2 x 2 mm

longest readout trace - 85 mm

single channel capacitance - 5 pF

outermost radius - 4.5 x 4.5 mm

longest readout trace - 16 mm

single channel capacitance - 12 pF

OUTPUT CHARGE -

follows a broad Landau distribution for single MIP

most probable value - 4 fC

OTHER CHARACTERISTICS -

detectors are ac-coupled

polysilicon biased (10 M-ohm typical)